

# **E225C – Lecture 16**

## **OFDM Introduction**

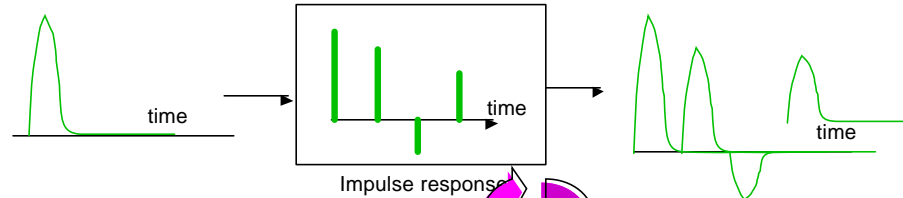
EE225C

## **Introduction to OFDM**

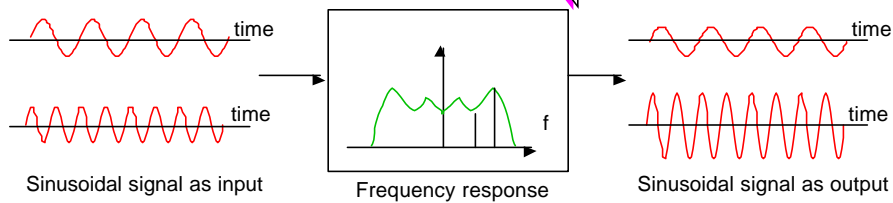
- Basic idea
  - » Using a large number of parallel narrow-band sub-carriers instead of a single wide-band carrier to transport information
- Advantages
  - » Very easy and efficient in dealing with multi-path
  - » Robust against narrow-band interference
- Disadvantages
  - » Sensitive to frequency offset and phase noise
  - » Peak-to-average problem reduces the power efficiency of RF amplifier at the transmitter
- Adopted for various standards
  - DSL, 802.11a, DAB, DVB

## Multipath can be described in two domains: time and frequency

Time domain: Impulse response

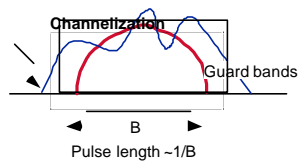


Frequency domain: Frequency response



## Modulation techniques: monocarrier vs. multicarrier

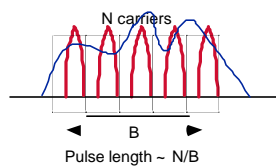
Channel



– Data are transmitted over **only one carrier**

### Drawbacks

- Selective Fading
- Very short pulses
- ISI is comparatively long
- EQs are then very long
- Poor spectral efficiency because of band guards



– Data are shared among **several carriers** and simultaneously transmitted

### Advantages

- Flat Fading per carrier
- N long pulses
- ISI is comparatively short
- N short EQs needed
- Poor spectral efficiency because of band guards

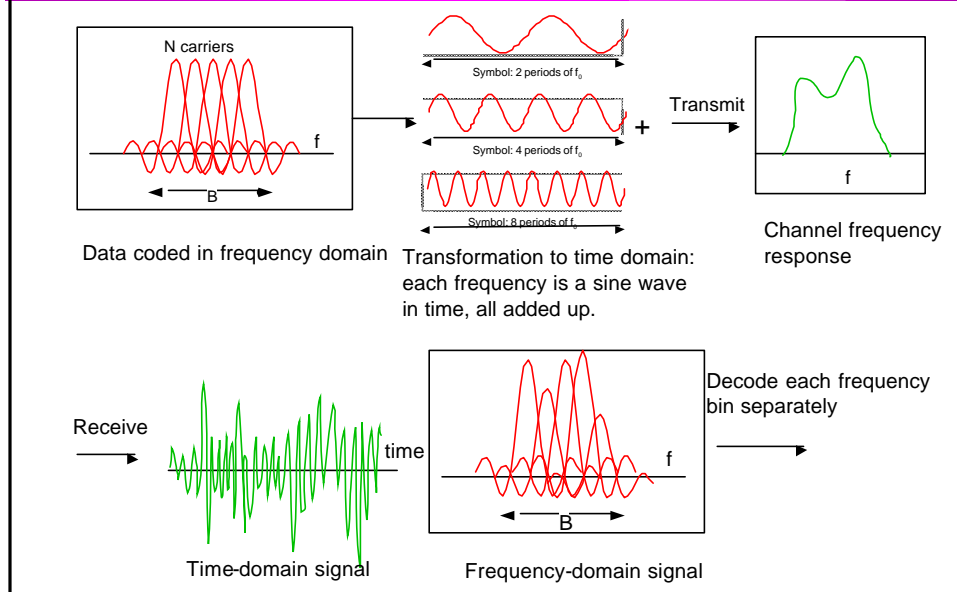
Similar to FDM technique

### Furthermore

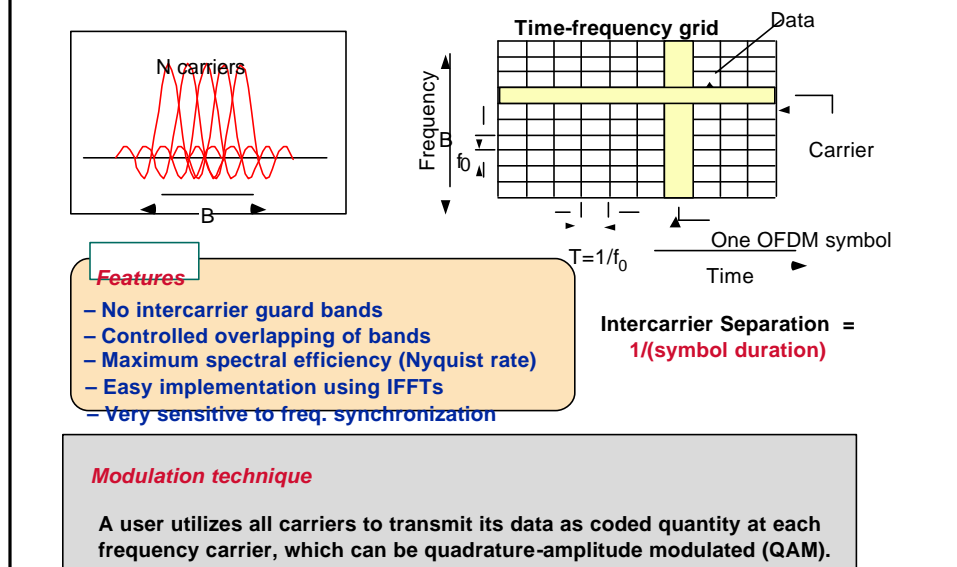
- It is easy to exploit Frequency diversity
- It allows to deploy 2D coding techniques
- Dynamic signalling

To improve the **spectral efficiency**:  
Eliminate band guards between carriers  
To use **orthogonal carriers** (allowing overlapping)

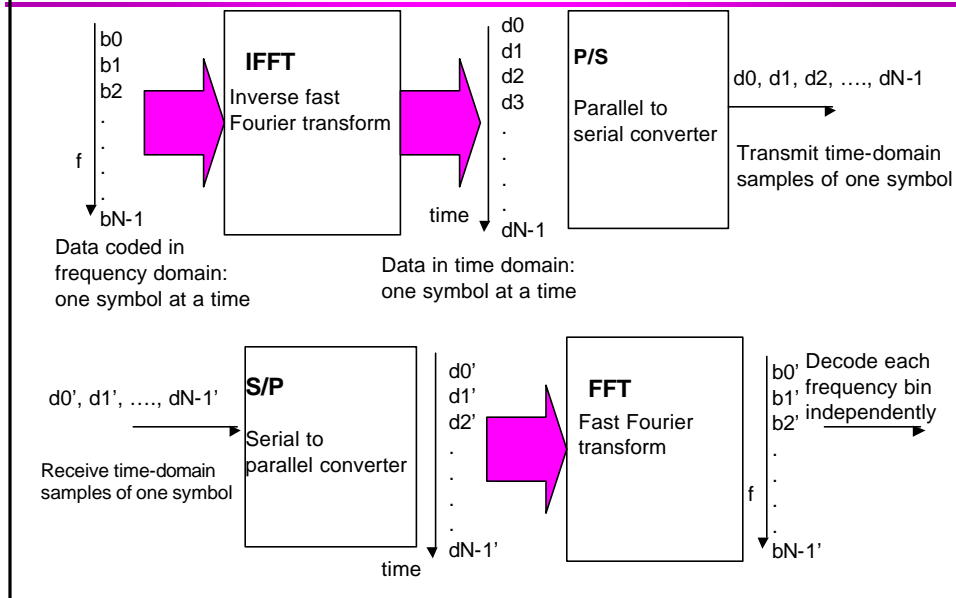
# Orthogonal Frequency Division Modulation



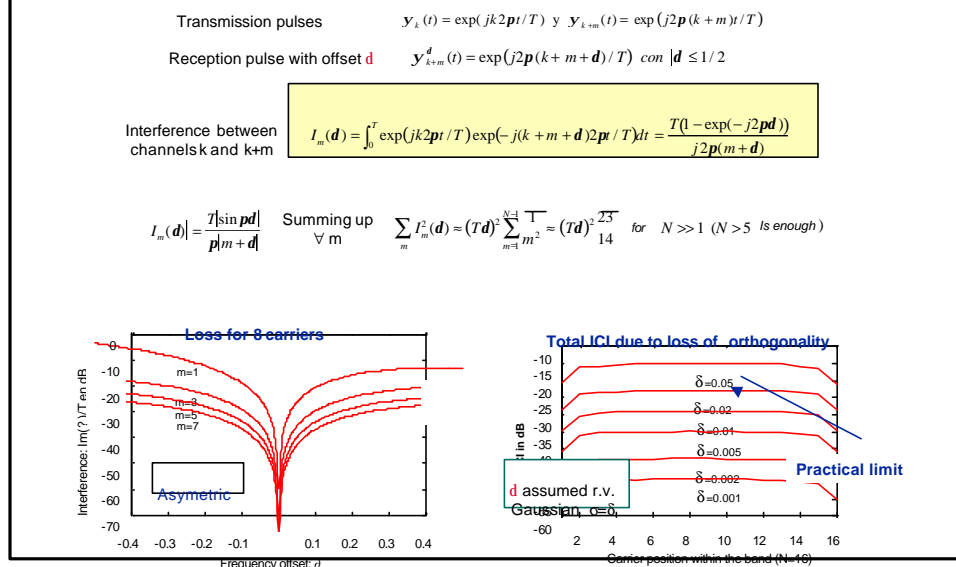
## OFDM uses multiple carriers to modulate the data



# OFDM Modulation and Demodulation using FFTs



# Loss of orthogonality (by frequency offset)



## Loss of orthogonality (time)

Let us assume a misadjustment  $\tau$

$$X_i = c_0 \int_{-T/2}^{T/2+t} y_k(t) y_i^*(t-t) dt + c_1 \int_{-T/2+t}^{T/2} y_k(t) y_i^*(t-t) dt$$

2 consecutive symbols

Then  $|X_i| = \begin{cases} 2T \frac{\sin m\pi \frac{t}{T}}{m\pi} & c_0 \neq c_1 \\ 0, & c_0 = c_1 \end{cases}$  if  $m=k-l$

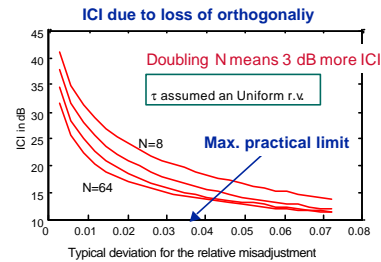
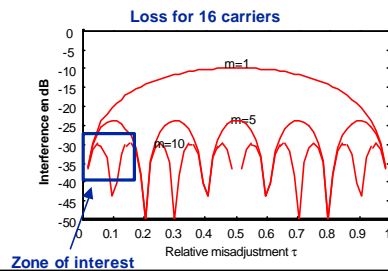
Or approximately, when  $\tau \ll T$ ,  $\frac{|X_i|}{T} = \frac{2m\pi}{m\pi} = 2 \frac{t}{T}$  independent on  $m$

In average, the interfering power in any carrier is

$$E \left[ \frac{|X_i|^2}{T^2} \right] = 4 \left( \frac{t}{T} \right)^2 \frac{1}{2} + 0 \frac{1}{2} = 2 \left( \frac{t}{T} \right)^2$$

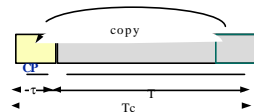
$$ICI = 20 \log \left( \sqrt{2} \frac{t}{T} \right), \quad t \ll T$$

Per carrier



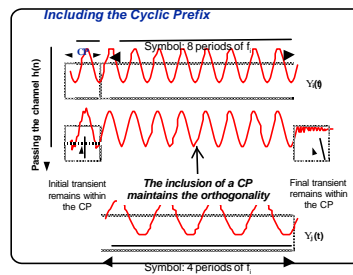
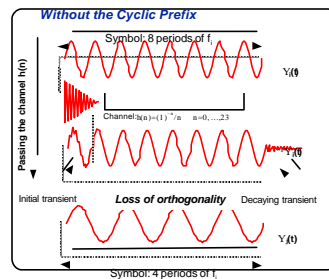
## Including a “cyclic prefix”

To combat the time dispersion: including ‘special’ time guards in the symbol transitions



Furthermore it converts Linear conv. = Cyclic conv.

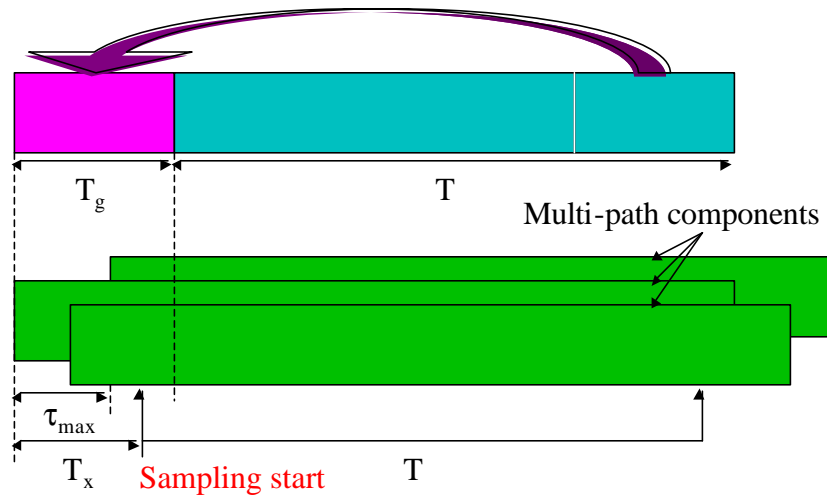
(Method: overlap-save)



CP functions:

- It acomodates the decaying transient of the previous symbol
- It avoids the initial transient reaches the current symbol

# Cyclic Prefix



## 802.11a System Specification



Short training sequence:  
AGC and frequency offset

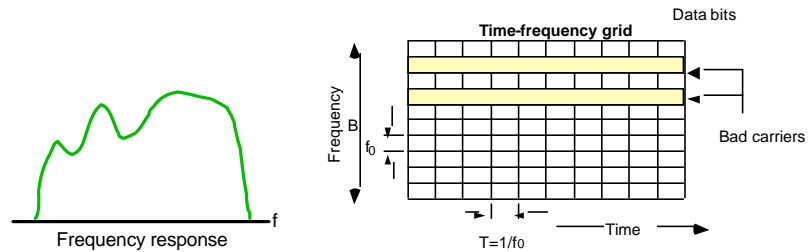
Long training sequence:  
Channel estimation

- Sampling (chip) rate: 20MHz
- Chip duration: 50ns
- Number of FFT points: 64
- FFT symbol period: 3.2 $\mu$ s
- Cyclic prefix period: 16 chips or 0.8 $\mu$ s
  - » Typical maximum indoor delay spread < 400ns
  - » OFDM frame length: 80 chips or 4 $\mu$ s
  - » FFT symbol length / OFDM frame length = 4/5
- Modulation scheme
  - » QPSK: 2bits/sample
  - » 16QAM: 4bits/sample
  - » 64QAM: 6bits/sample
- Coding: rate  $\frac{1}{2}$  convolutional code with constraint length 7

## Frequency diversity using coding

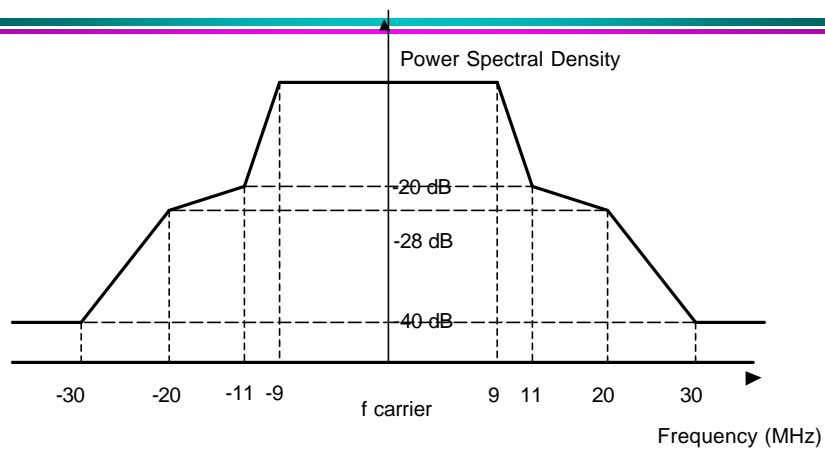
**Random errors:** primarily introduced by thermal and circuit noise.

**Channel-selected errors:** introduced by magnitude distortion in channel frequency response.



Errors are no longer random. Interleaving is often used to scramble the data bits so that standard error correcting codes can be applied.

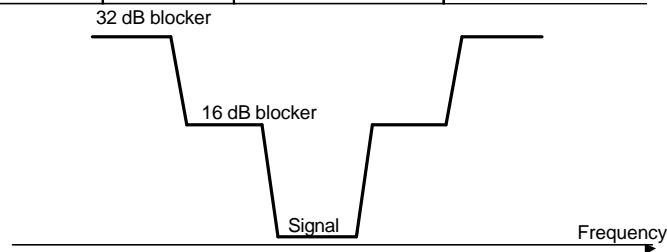
## Spectrum Mask



- Requires extremely linear power amplifier design.

## Adjacent Channel and Alternate Channel Rejection

Date rate	Minimum Sensibility	Adjacent Channel Rejection	Alternate Channel rejection
6 M b p s	-82 d B m	16 d B	32 d B
12 M b p s	-79 d B m	13 d B	29 d B
24 M b p s	-74 d B m	8 d B	24 d B
36 M b p s	-70 d B m	4 d B	20 d B
54 M b p s	-65 d B m	0 d B	15 d B



- Requires joint design of the anti-aliasing filter and ADC.

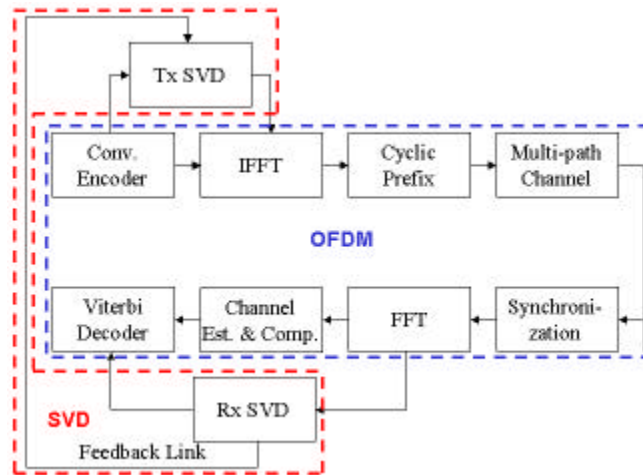
## OFDM Receiver Design

Yun Chiu, Dejan Markovic, Haiyun Tang,  
Ning Zhang

EE225C Final Project Report, 12 December  
2000

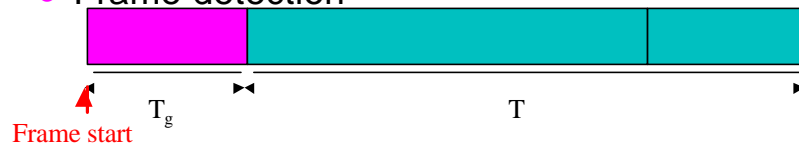


# OFDM System Block Diagram



## Synchronization

- Frame detection



- Frequency offset compensation

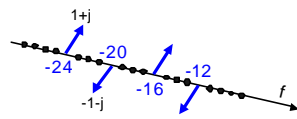
$$e^{j2\pi\Delta fct}$$

- Sampling error

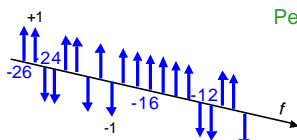
- » Usually less 100ppm and can be ignored
  - 100ppm = off 1% of a sample every 100 samples



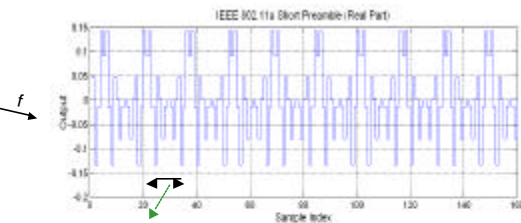
# Short & Long Preambles



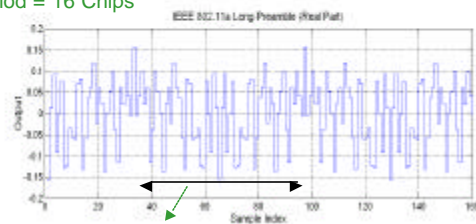
Short Preamble



Long Preamble

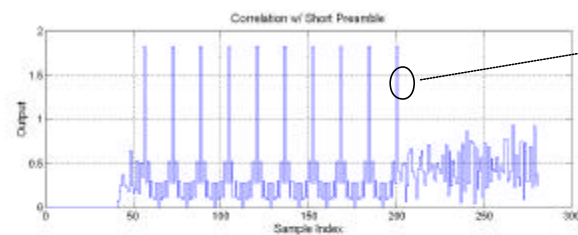


Period = 16 Chips



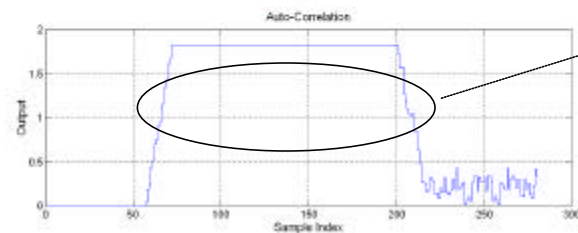
Period = 64 Chips

## Correlation of Short Preamble



Correlation

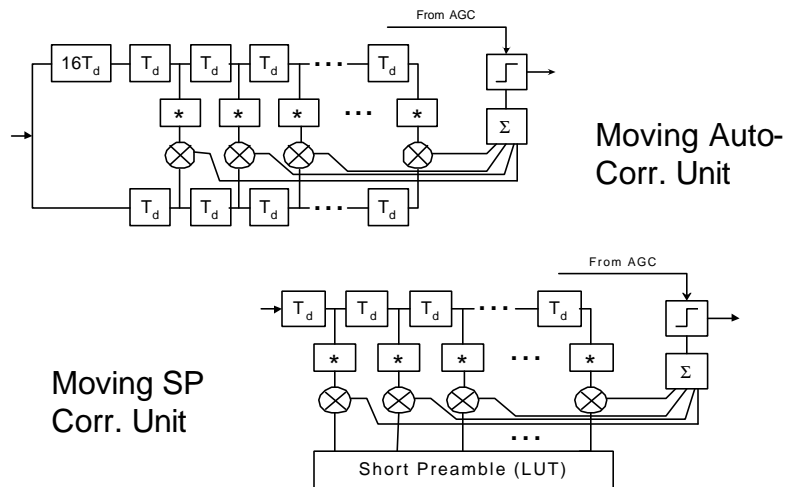
Fine Timing



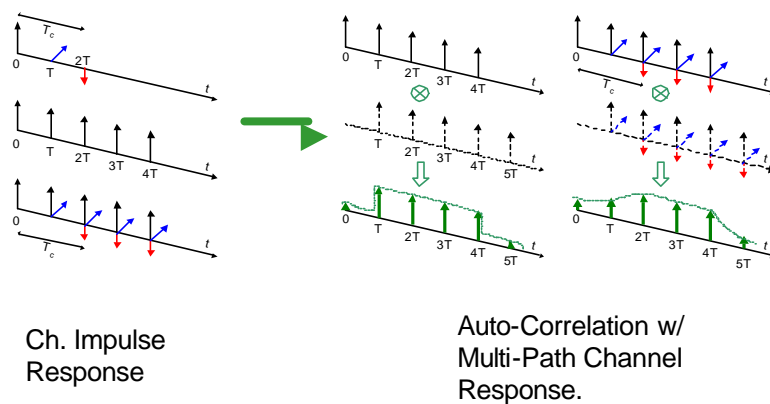
Auto-Correlation

Coarse Timing

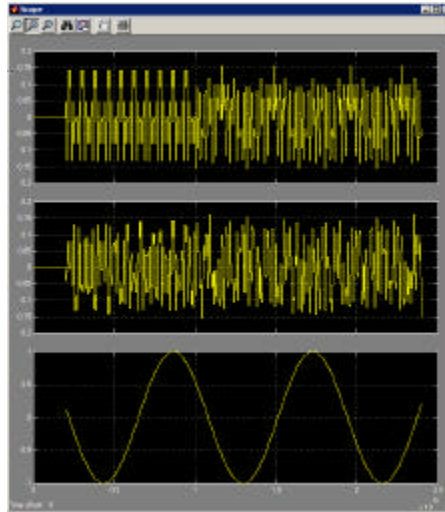
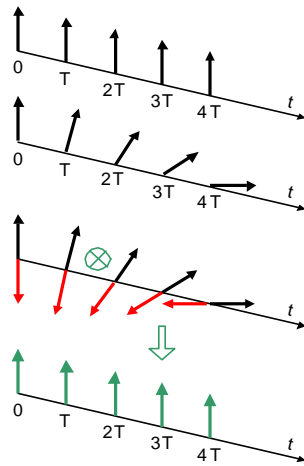
# Synchronization



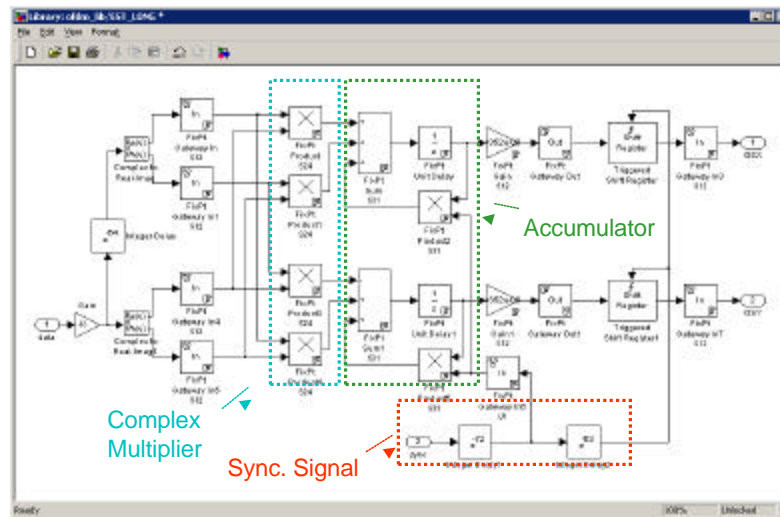
## Impairments: Multi-Path Channel



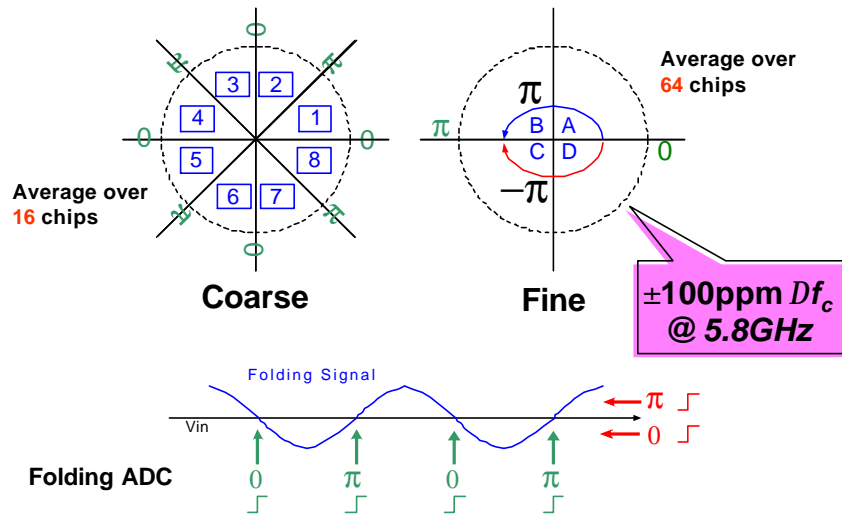
## Impairments: Frequency Offset



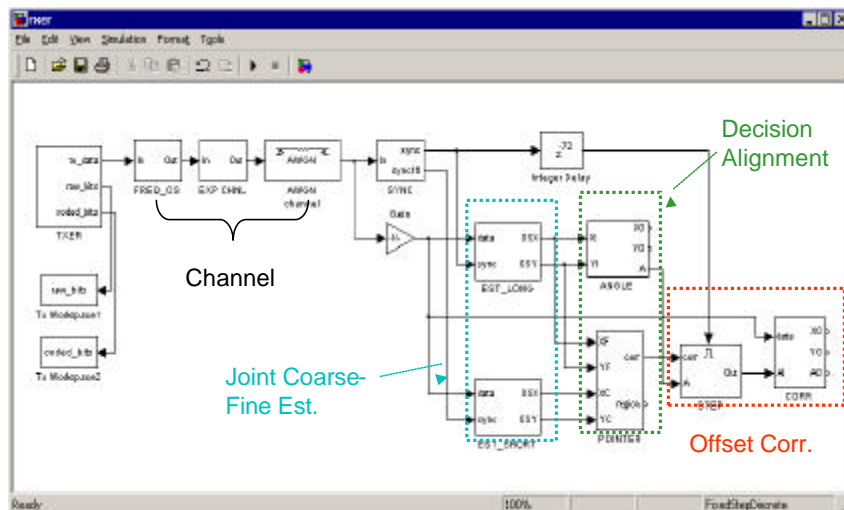
## Fine Frequency Offset Est.



## Coarse-Fine Joint Estimation & Decision Alignment Error Correction



## Frequency Offset Compensation



## Performance Summary

<i>Parameters</i>	<i>Metrics</i>
<i>Number of sub-carriers</i>	<b>48 data +4 pilot</b>
<i>OFDM symbol freq.</i>	<b>4 ms</b>
<i>Modulation Scheme</i>	<b>BPSK up to 64-QAM</b>
<i>Sampling clock freq.</i>	<b>20 MHz</b>
<i>Sync. Frame Start Accuracy</i>	<b>± 8 chips (CP = 16 chips)</b>
<i>Freq. Offset Est. Range</i>	<b>± 5p = ± 100ppm @ 5.8 GHz</b>
<i>Freq. Offset Est. Accuracy</i>	<b>1% (@ 15dB SNR)</b>
<i>Critical path delay</i>	<b>12.7 ns</b>
<i>Silicon area</i>	<b>397,080 mm<sup>2</sup></b>
<i>Total power consumption</i>	<b>3.4 mW @ 20 MHz</b>